

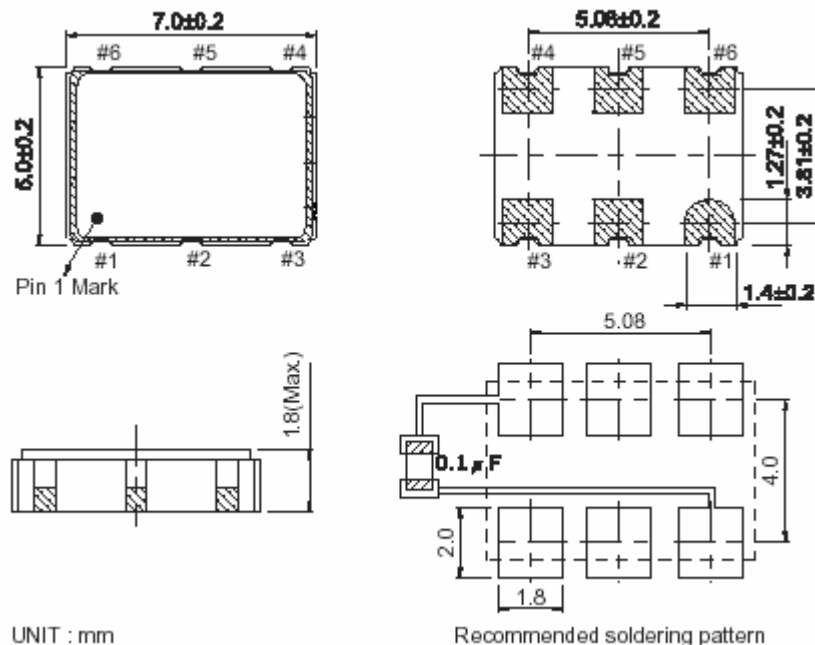
Features

- LVPECL, Extended Temperature available
- Ceramic SMD Package, Seam sealed, 7x5x1.8mm
- 2.5V / 3.3V Operation
- RoHS Compliant

Specification

Parameter	Characteristic
Frequency Range	1.000MHz ~ 800.0000MHz
Frequency Stability	+/- 50 ppm std. (See Table 4) Inclusive of operating temperature
Operating Temperature Range	0 ~ 70°C std. (See Table 5)
Storage Temperature Range	-55 ~ +125°C
Input Voltage	3.3Vdc +/- 5% std.
Input Current	100mA max
Output 0 Level (Vol)	Vcc-1.63Vdc max
Output 1 Level (Voh)	Vcc-1.02Vdc min
Symmetry (Duty Cycle)	40/60% @ Vcc-1.3Vdc std. (See Table 6)
Rise & Fall Time	0.6nS typical, 1.0nS max
Start up time	10mS max
Output Load	50Ω to Vcc-2.0Vdc
Tri-state Output (Pin# 1)	High or Open : Oscillation, Low : High Impedance
Aging	+/- 3 ppm max / year
Phase Jitter (12KHz~20MHz)	1pS RMS max (See Table A)

Drawing



Pin Connection

1. E/D or N/C
2. N/C or E/D
3. Ground
4. Output
5. Comp. Output
6. Vcc

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Ordering Guide

Typical P/N : C57XP - 155.52M - 3 - 50 A S1 T1 -TR

1 2 3 4 5 6 7 8

1. Package C57XP = 7x5x1.8mm

(7x5mm Ceramic LVPECL SMD Oscillator)

2. Frequency range : 1 to 800MHz

3. Input Voltage : 2 = 2.5V / 3 = 3.3V

4. Frequency Stability

00 : +/- 100ppm

50 : +/- 50ppm

25 : +/- 25ppm

5. Operating Temperature Range

A : 0~70°C

B : -20~70°C

C : -40~85°C

D : -10~70°C

* : The others

6. Symmetry (Duty Cycle)

S1 : 45/55% at Vcc - 1.3Vdc

S2 : 40/60% at Vcc - 1.3Vdc

7. Pin#1 & Pin#2 Connection

T1 : Pin#1 E/D, Pin#2 N/C Standard

T2 : Pin#1 N/C, Pin#2 E/D

8. Packing

TR : Tape and Reel

BU : Bulk

TU : Tube

A. Phase Jitter

1MHz ≤ F < 9.7MHz : 3pS RMS max

9.7MHz ≤ F ≤ 200MHz : 1pS RMS max

200.001MHz < F ≤ 800MHz : 3pS RMS max

(12KHz ~ 20MHz)