

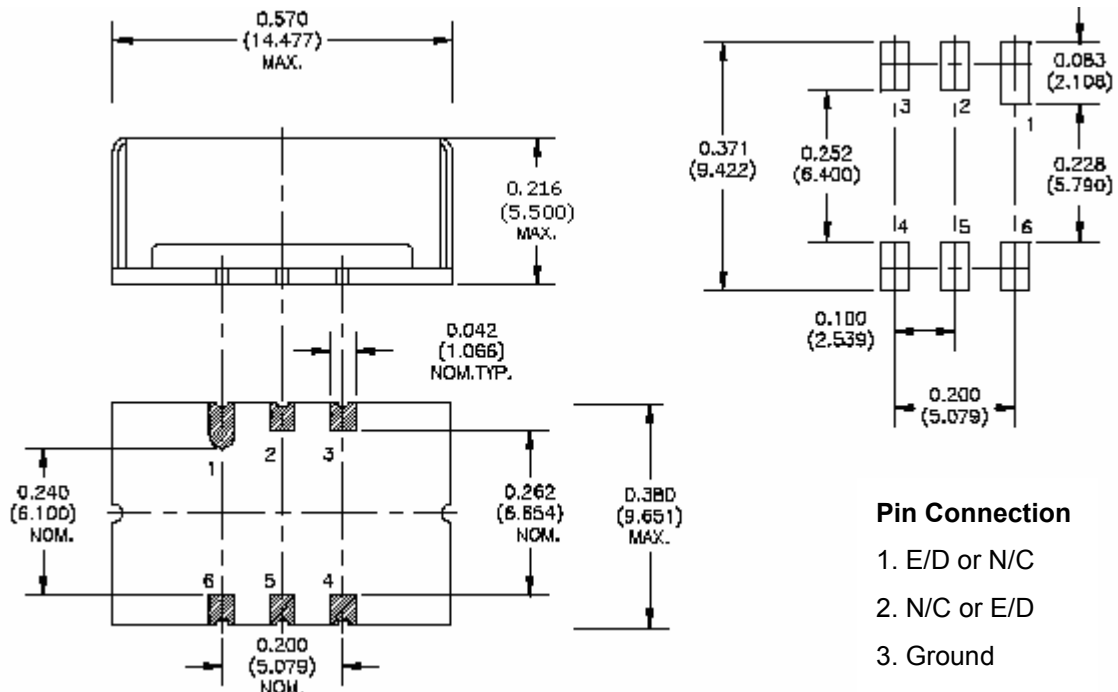
Features

- LVDS, Extended Temperature available
- PCB type 6pads, Sealed crystal built in, 14x9.8x5.5mm
- 3.3V / 5.0V Operation
- RoHS Compliant

Specification

Parameter	Characteristic
Frequency Range	8.000MHz ~ 622.0800MHz
Frequency Stability	+/- 50 ppm std. (See Table 4) Inclusive of operating temperature
Operating Temperature Range	0 ~ 70°C std. (See Table 5)
Storage Temperature Range	-55 ~ +125°C
Input Voltage	3.3Vdc +/- 5% std.
Input Current	250mA max (See Table B)
Output 0 Level (Vol)	Vcc-2.175Vdc max
Output 1 Level (Voh)	Vcc-1.925Vdc min
Symmetry (Duty Cycle)	40/60% @ 50% of waveform std. (See Table 6)
Rise & Fall Time	0.6nS typical, 1.0nS max
Start up time	10mS max
Output Load	50Ω Differential Load
Tri-state Output (Pin# 2)	High or Open : Oscillation, Low : High Impedance
Aging	+/- 3 ppm max / year
Phase Jitter (12KHz~20MHz)	1pS RMS max (See Table A)

Drawing



Pin Connection

1. E/D or N/C
2. N/C or E/D
3. Ground
4. Output
5. Comp. Output
6. Vcc

Ordering Guide

Typical P/N : F6XL - 155.52M - 3 - 50 A S1 T2 -TR

1

2

3

4

5

6

7

8

1. Package F6XL = 14x9.8x5.5mm, 6pads

(FR4, PCB type 6pads LVDS Oscillator)

2. Frequency range : 8 to 622.08MHz

3. Input Voltage : 3 = 3.3V / 5 = 5.0V

4. Frequency Stability

00 : +/- 100ppm

50 : +/- 50ppm

25 : +/- 25ppm

5. Operating Temperature Range

A : 0~70°C

B : -20~70°C

C : -40~85°C

D : -10~70°C

* : The others

6. Symmetry (Duty Cycle)

S1 : 45/55% at 50% of waveform

S2 : 40/60% at 50% of waveform

7. Pin#1 & Pin#2 Connection

T1 : Pin#1 E/D, Pin#2 N/C (Under Development)

T2 : Pin#1 N/C, Pin#2 E/D Standard

8. Packing

TR : Tape and Reel

BU : Bulk

TU : Tube

A. Phase Jitter

8MHz ≤ F ≤ 200MHz : 1pS RMS max

200.001MHz < F ≤ 622.08MHz : 3pS RMS max

(12KHz ~ 20MHz)

B. Input Current

3.3V : 80mA ~ 200mA max

5.0V : 120mA ~ 250mA max